

FOR IMMEDIATE RELEASE

**XILINX ANNOUNCES INDUSTRY'S FIRST MEMORY CONTROLLER SOLUTION
FOR QUAD DATA RATE SRAMS**

Xilinx reference design based on Spartan-II FPGAs supports data throughput of 7.2 Gbps

SAN JOSE, Calif., February 22, 2000—Xilinx Inc., (NASDAQ:XLNX) today announced the availability of a high-performance reference design for Quad Data Rate (QDR™) SRAMs based on low-cost Spartan™-II FPGAs. Cypress Semiconductor Corp. (NYSE:CY), IDT (NASDAQ:IDTI) and Micron Technology Inc. (NYSE:MU) defined and developed the new QDR SRAM architecture for future high-performance communications applications. The Xilinx Spartan-II FPGAs can be used to implement the control logic for the memory interface for the SRAM market. The new QDR architecture targets next-generation high-performance switches and routers.

Xilinx Spartan-II FPGAs sustain data throughput of up to 7.2 Gigabits per second (Gbps), operating at clock rates of 100 MHz. Spartan-II FPGAs support delay locked loops (DLLs), high speed transceiver logic (HSTL) I/Os, and BlockRAM, which are essential in interfacing with QDR SRAMs. The memory controller circuit described in the Xilinx reference design can connect four QDR SRAMs to a host CPU. Each QDR SRAM has separate control signals for the read and write ports, while the address and data ports are common for all the SRAMs. The controller supports concurrent double data rate (DDR) operations on all of the input and output signals and allows byte-write operations into the memory. Operating in the single-clock mode the controller significantly simplifies the memory interface.

"Xilinx is the first semiconductor company to deliver a QDR SRAM memory controller solution. We are very pleased to be working with the QDR SRAM consortium in creating next-generation high performance solutions," said Kapil Shankar, senior marketing director for the High-volume Business Unit at Xilinx. "This Spartan-II reference design further extends our leadership role in providing cost-effective solutions for a wide range of applications."

"The QDR SRAM consortium is pleased to work closely with Xilinx in providing a Spartan-II FPGA based high-performance, cost-effective memory controller solution," said Matthew Arcoleo, strategic marketing manager for the Memory Products Division at Cypress Semiconductor. "The availability of a free memory controller reference design will proliferate the use of QDR SRAM based networking solutions."

The development of the unique QDR SRAM architecture included extensive input from networking industry leaders (www.qdrsram.com). The devices are designed to greatly increase memory bandwidth compared with existing SRAM solutions and will serve as the main memory for look-up tables, linked lists, and controller buffer memory. This family of high-performance QDR SRAMs has been defined to provide customers with multiple supply sources.

The consortium worked closely to ensure that customers will have second sources for the new QDR SRAMs by developing pin and function compatible products. Each partner is providing system expertise and product direction, giving customers the benefit of a wide range of market experience and innovative technology.

Spartan-II FPGAs

The Spartan-II family delivers 100,000 system gates for under \$10 at speeds of 200 MHz and beyond, offering unmatched design flexibility. These low-power 2.5-volt devices feature I/Os that operate at 3.3 volts with full 5-volt tolerance. Spartan-II devices also feature multiple delay locked loops (DLLs), on-chip RAM (block and distributed), and the versatility of SelectI/O technology supporting over 16 high-performance interface standards. All these features in a device that offers unlimited programmability and can be upgraded in the field.

Xilinx at Work

Xilinx at Work in High Volume Applications focuses on creating complete system solutions for various high volume market segments. Xilinx at Work consists of collateral which includes reference designs, application notes, white papers, tutorials, and multiple examples illustrating the use of Xilinx solutions in various high-volume applications.

Product availability

The Xilinx QDR SRAM memory controller reference design is available free of charge from the Xilinx website.

About Xilinx

Xilinx is the leading innovator of complete programmable logic solutions, including advanced integrated circuits, software design tools, predefined system functions delivered as cores, and unparalleled field engineering support. Founded in 1984 and headquartered in San Jose, Calif., Xilinx invented the field programmable gate array (FPGA) and fulfills more than half of the world demand for these devices today. Xilinx solutions enable customers to reduce significantly the time required to develop products for the computer, peripheral, telecommunications, networking, industrial control, instrumentation, high-reliability/military, and consumer markets. For more information, visit the Xilinx website at www.xilinx.com.